

FIG. 1

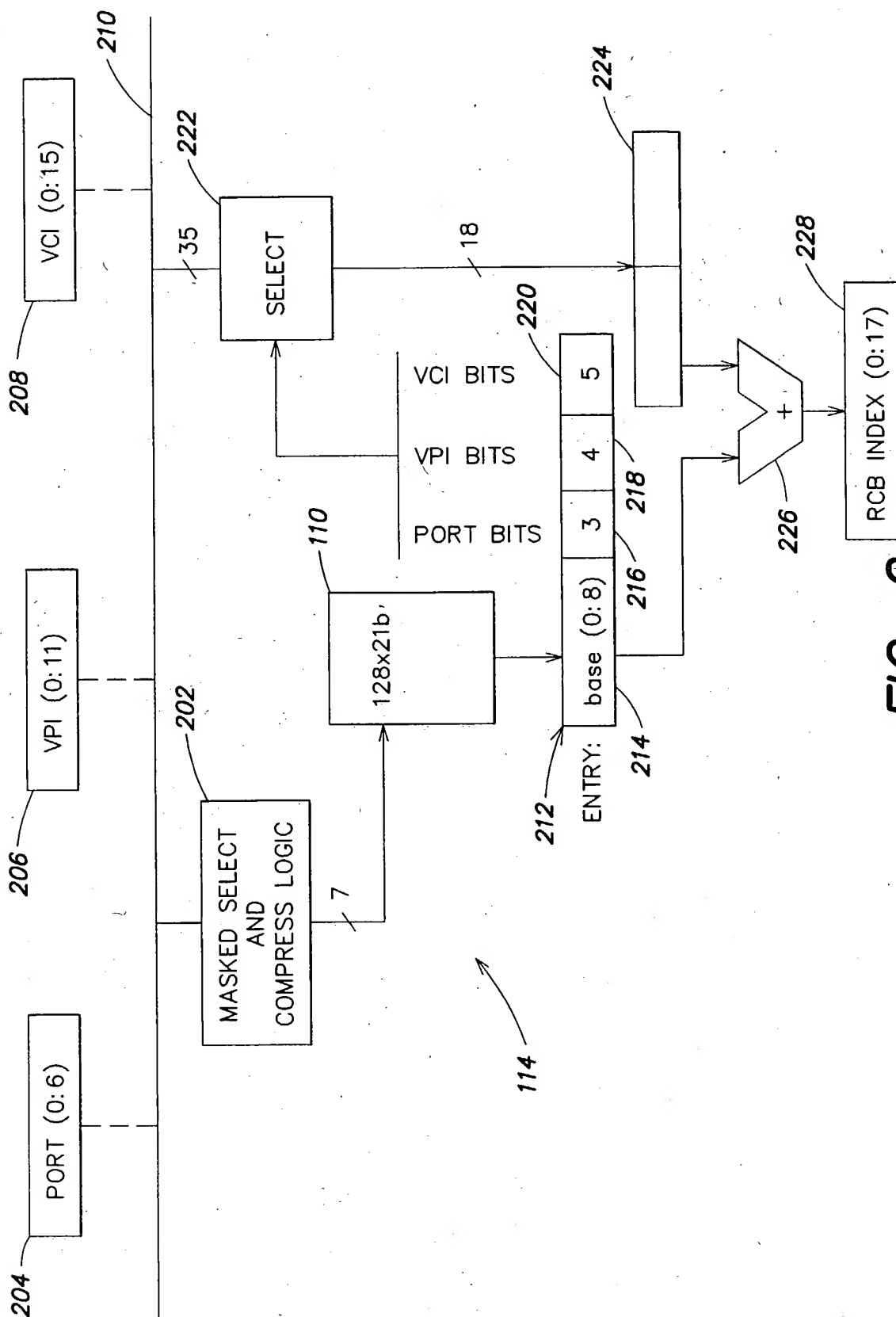


FIG. 2

110

214

216

218

220

Entry in First Memory	First Memory Base Offset	Number of LSBs of the Port No. to be Selected	Number of LSBs of the VPI to be Selected	Number of LSBs of the VCI to be Selected
Entry 1	0 0000 0000	000	1000	0 1000
Entry 2	0 1000 0000	000	0010	0 1100
Entry 3	0 1010 0000	000	0100	0 0100
Entry 4	0 1010 0001	-	-	-

302a

302b

302c

302d

FIG. 3

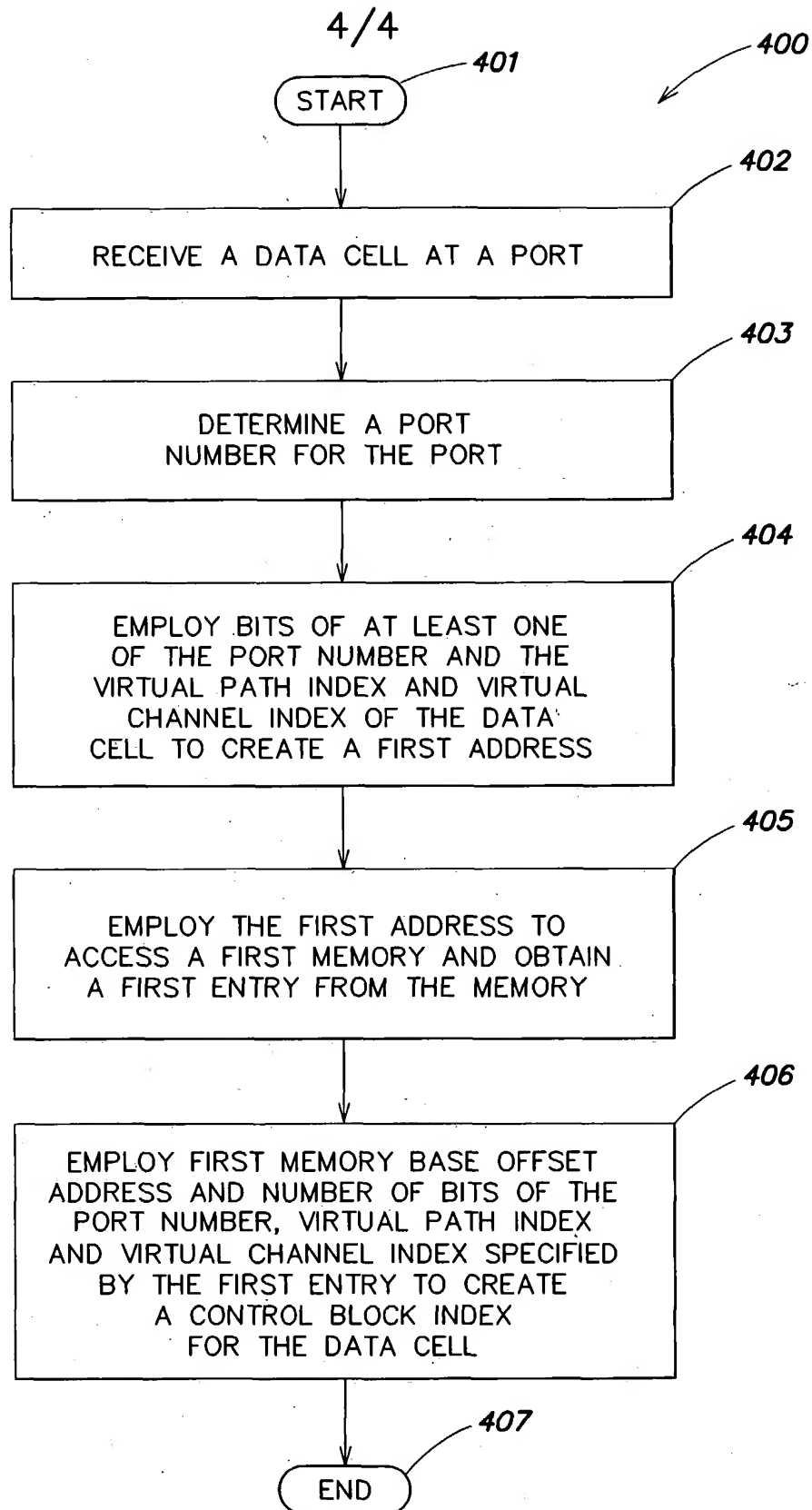


FIG. 4